

Display Driver Circuits (cont'd)

<p>ECG I_o Max* 2031 50 mA</p> <p>16-Pin DIP See Fig. D8</p> <p>$V_{EE} = -55\text{ V}$ $V_{CC} = \text{GND}$</p> <p>7 HV Digit Driver (Active Low Input)</p>	<p>ECG I_o Max* 2032 30 mA</p> <p>16-Pin DIP See Fig. D8</p> <p>$V^+ = +5\text{ V}$</p> <p>BCD-to-7 Segment Decoder/Driver, w/Constant Current Outputs</p>	<p>ECG I_o Max* 2033 1.5 A</p> <p>16-Pin DIP See Fig. D9</p> <p>$V_{CC} = +5\text{ V}$</p> <p>4 Unit Darlington Array/Driver (Active Low Input)</p>
<p>ECG I_o Max* 2070 150 mA 2071 150 mA</p> <p>18-Pin DIP See Fig. D10</p> <p>$V_{CC} = +6\text{ V}$</p> <p>7 Unit Transistor Array/Driver, w/Clamp Diode and Strobe</p>	<p>ECG I_o Max* 2072 320 mA 2073 320 mA</p> <p>16-Pin DIP See Fig. D8</p> <p>$V_{CC} = +6\text{ V}$</p> <p>6 Unit Transistor Array/Driver, w/Clamp Diode and Strobe</p>	<p>ECG I_o Max* 2075 320 mA</p> <p>14-Pin DIP See Fig. D6</p> <p>$V_{CC} = +6\text{ V}$</p> <p>5 Unit Transistor Array/Driver, w/Strobe</p>
<p>ECG I_o Max* 2077 150 mA 2082 100 mA</p> <p>14-Pin DIP See Fig. D6 See 14-Pin DIP-ET See Fig. D7</p> <p>6 Unit Darlington Array/Driver, w/Clamp Diode</p>	<p>ECG I_o Max* 2078 500 mA 2084 500 mA</p> <p>14-Pin DIP See Fig. D6 See 14-Pin DIP-ET See Fig. D7</p> <p>5 Unit Darlington Array/Driver</p>	<p>ECG I_o Max* 2079 400 mA</p> <p>16-Pin DIP Fig. D8</p> <p>7 Unit Darlington Array/Driver</p>
<p>ECG I_o Max* 2080 100 mA</p> <p>18-Pin DIP See Fig. D10</p> <p>$V_{CC} = +8\text{ V}$</p> <p>7 Unit Transistor Array/Driver, w/strobe</p>	<p>ECG I_o Max* 2083 150 mA</p> <p>14-Pin DIP-ET See Fig. D7</p> <p>6 Unit Darlington Array/Driver</p>	<p>ECG I_o Max* 2085 1.5 A 2086 1.5 A</p> <p>16-Pin DIP See Fig. D8</p> <p>4 Unit Darlington Array/Driver</p>

* I_o Max = Current Per Transistor

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