

HTL (cont'd)

<p>ECG9671 14-Pin DIP See Fig. D6 Triple 3-Input NAND Gate (Active Pull-Up), $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$</p>	<p>ECG9672 14-Pin DIP See Fig. D6 Quad 2-Input NAND Gate (Active Pull-Up), $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$</p>	<p>ECG9673 14-Pin DIP See Fig. D6 Dual-Input AND/OR/Invert Gate (Active Pull-Up), $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$</p>
<p>ECG9674 14-Pin DIP See Fig. D6 Dual 2-Input AND/OR/Invert Gate (Passive Pull-Up), $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$</p>	<p>ECG9675 14-Pin DIP See Fig. D6 Dual Pulse Stretcher, $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$</p>	
<p>ECG9677 16-Pin DIP See Fig. D8 Hex Inverter with Strobe (Active Pull-Up), $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 16, GND} = \text{Pin 8}$</p>	<p>ECG9678 16-Pin DIP See Fig. D8 Hex Inverter with Strobe (Without Output Resistors), $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 16, GND} = \text{Pin 8}$</p>	<p>ECG9679 14-Pin DIP See Fig. D6 Dual Lamp/Line Driver, $V_{CC} = +15\text{ V (Nom.)}$</p>
<p>ECG9680* 14-Pin DIP See Fig. D6 Hex Inverter (Active Pull-Up), $V_{CC} = +15\text{ V (Nom.)}$ * Discontinued</p> <p>ECG9681 Hex Inverter (Open Collector), $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$</p>	<p>ECG9682 16-Pin DIP See Fig. D8 Quad Latch, $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 16, GND} = \text{Pin 8}$</p>	<p>ECG9683 14-Pin DIP See Fig. D6 Quad 2-Input Exclusive OR Gate, $V_{CC} = +15\text{ V (Nom.)}$</p> <p>$V_{CC} = \text{Pin 14, GND} = \text{Pin 7}$</p>